## UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO. : 6,974,333 B2 Page 1 of 1

APPLICATION NO. : 10/813945

DATED : December 13, 2005

INVENTOR(S) : Douglas Glenn Wildes, Robert Stephen Lewandowski and Geir Ultveit Haugen

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Col. 17 lines 54-67 & Col. 18 lines 1-11 Claims 20 and 21 as they now read should be deleted in their entirety.

Claims 20 and 21 should read as follows:

## --20. An apparatus comprising:

first, second and third stacked connections, a first spacer disposed between said first and second stacked connections, and a second spacer disposed between said second and third stacked connections, each of said first, second and third stacked connections comprising a respective plurality of circuit boards,

wherein said second spacer comprises a hole and each circuit board of said second stacked connection comprises a respective opening, further comprising a first alignment pin anchored in said first spacer and projecting through said openings in said circuit boards of said second stacked connection and into said hole in said second spacer; and

a clamp that clamps said first through third stacked connections and said first and second spacers together, said clamp comprising a bolt, wherein each of said first through third stacked connections has an opening penetrated by said bolt, and each of said first and second spacers has a hole penetrated by said bolt.

21. The apparatus as recited in claim 20, further comprising:

a fourth stacked connection comprising a plurality of circuit boards, and a third spacer disposed between said third and fourth stacked connections, wherein said third spacer comprises a hole and each circuit board of said third stacked connection comprises a respective opening, further comprising a second alignment pin anchored in said second spacer and projecting through said openings in said circuit boards of said third stacked connection and into said hole in said third spacer.--

Signed and Sealed this

Ninth Day of March, 2010

David J. Kappos Director of the United States Patent and Trademark Office